



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: DEBASHIS BHATTACHARYA et al.
Serial No.: 09/896,059
For: METHOD FOR AUTOMATED DESIGN OF INTEGRATED
CIRCUITS WITH TARGETED QUALITY OBJECTIVES USING
DYNAMICALLY GENERATED BUILDING BLOCKS
Filed: June 29, 2005
Examiner: Thompson, A. M.
Art Unit: 2825
Confirmation No.: 9475
Customer No.: 27623 Attorney Docket No.: 162.7107USU

AMENDMENT UNDER RULE 116

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
31-May-2006

Sir:

In reply to the final Office Action dated February 9, 2006, please enter the following amendments in the consideration of the above patent application.

Amendment to the claims begins on page 2.

Remarks begin on page 17.